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Dynamic Hardware Obfuscation Techniques for Mixed-Signal SoC Security

P. Ravi Teja, N. Venkat Rao

P.G. Student, Department of Electronics and Communications Engineering, SVEC, Tirupati, India

Associate Professor, Department of Electronics and Communications Engineering, SVEC, Tirupati, India

ABSTRACT: The growing complexity and widespread adoption of mixed-signal System-on-Chip (SoC) designs have made them critical in applications such as IoT devices, automotive systems, and healthcare electronics. However, this integration of analog and digital components introduces significant security vulnerabilities, including hardware reverse engineering, fault injection, and side-channel attacks. Traditional static obfuscation methods, primarily focused on digital domains, are inadequate for mixed-signal systems where analog components and the analog-digital interface are prime targets for adversarial exploitation.

This paper proposes Dynamic Hardware Obfuscation Techniques for Mixed-Signal SoC Security, a novel framework that secures both analog and digital domains through runtime reconfigurable obfuscation mechanisms. The framework dynamically alters circuit parameters in analog components, masks side-channel signatures, and secures analog-to-digital interfaces, preventing attackers from extracting sensitive information or introducing malicious manipulations. For digital logic, dynamic key-based obfuscation techniques and reconfigurable logic are integrated to protect against reverse engineering and hardware Trojans.

The proposed techniques are validated using hardware simulations and prototype implementations, demonstrating improved resilience against side-channel analysis, fault injection, and reverse engineering. Results show minimal area and power overheads, ensuring applicability in resource-constrained environments. This work highlights the importance of adaptability and real-time security in mixed-signal SoCs, paving the way for robust and scalable protection mechanisms in next-generation systems.

KEYWORDS: Mixed-Signal SoC Security, Dynamic Hardware Obfuscation, Side-Channel Attack Mitigation Analog-Digital Interface Protection, Fault Injection.

I. INTRODUCTION

The rapid advancement of System-on-Chip (SoC) technology has enabled the integration of analog and digital components into compact, efficient, and high-performance systems. Mixed-signal SoCs are now ubiquitous in critical applications such as IoT devices, automotive electronics, healthcare equipment, and defense systems. These applications rely heavily on the interaction between analog and digital components, where analog circuits perform real-world signal acquisition and conditioning, and digital circuits handle computation, communication, and control. However, this convergence introduces significant security challenges, as hardware attacks exploit vulnerabilities in both domains and their critical interfaces.

While digital hardware security has been extensively studied, with techniques such as logic obfuscation, encryption, and secure boot, the analog domain and analog-digital interface remain underexplored. Analog components are highly susceptible to: Reverse Engineering: Attackers analyze circuits to extract design details or introduce malicious modifications. Side-Channel Attacks: Adversaries exploit power, timing, or electromagnetic emissions to leak sensitive information. Fault Injection Attacks: Techniques like voltage glitches or electromagnetic pulses can disrupt analog signals or trigger Trojan circuits. Analog-Digital Interface Exploitation: Weaknesses at the interface can be manipulated to propagate faults or tamper with signal integrity.

Current obfuscation techniques are predominantly static and focus on the digital domain. Static approaches fail to adapt to evolving attack strategies and do not adequately protect analog circuits or the analog-digital interface. Analog components, due to their continuous nature and sensitivity to environmental variations, require dynamic and adaptable protection mechanisms that can counter a range of physical and environmental attacks.

II. RELATED WORK

The growing adoption of mixed-signal System-on-Chip (SoC) designs has significantly enhanced performance and functionality in modern electronic systems. These systems integrate analog and digital components on a single chip, enabling real-world signal acquisition, processing, and control in applications like IoT, automotive systems, biomedical devices, and aerospace. While digital circuits have been the focus of extensive hardware security research, the analog domain and the analog-digital interface remain critical yet overlooked attack surfaces. This section provides an overview of hardware security challenges in mixed-signal SoCs and surveys related work in static and dynamic hardware obfuscation techniques.

III. PROPOSED DYNAMIC HARDWARE OBFUSCATION FRAMEWORK

The Proposed Dynamic Hardware Obfuscation Framework is designed to address the security vulnerabilities inherent in mixed-signal System-on-Chip (SoC) designs. It offers a unified approach to dynamically protect both analog and digital components, as well as the critical analog-digital interface, against advanced hardware attacks, including reverse engineering, fault injection, hardware Trojans, and side-channel analysis. Unlike static obfuscation techniques, the proposed framework introduces runtime adaptability, allowing the system to respond dynamically to potential threats.

- **Framework Overview**

The dynamic hardware obfuscation framework consists of three core layers that work collaboratively to secure the mixed-signal SoC: Dynamic Analog Obfuscation Layer: Secures analog components by obfuscating circuit behavior dynamically. Dynamic Digital Obfuscation Layer: Applies runtime reconfigurable logic and key-based obfuscation to protect digital components. Analog-Digital Interface Protection Layer: Safeguards the boundary between analog and digital domains using real-time monitoring and dynamic isolation techniques. A Threat Detection Engine acts as the orchestrator, identifying potential threats in real time and triggering appropriate obfuscation responses.

- **Dynamic Analog Obfuscation Layer**

The analog layer focuses on securing analog circuits such as sensors, amplifiers, ADCs (Analog-to-Digital Converters), and DACs (Digital-to-Analog Converters), which are critical to SoC functionality but prone to physical and environmental attacks. Parameter Randomization: Dynamically alters circuit parameters such as biasing currents, gain, capacitance, and frequency responses within acceptable operational limits. Randomization makes it difficult for attackers to reverse-engineer or predict the circuit behaviour. Noise Injection: Controlled noise is injected into analog circuits to obfuscate side-channel signatures (e.g., power consumption or electromagnetic emissions). The noise level is tuned to avoid interference with functional performance while masking sensitive signal patterns. Adaptive Gain Control: Dynamically adjusts the gain of amplifiers to prevent attackers from manipulating or analyzing input-output signal relationships. Fault Detection and Correction: Real-time monitoring circuits detect voltage glitches, temperature anomalies, or external tampering. Adaptive circuits dynamically isolate or correct faults to ensure continued operation.

- **Dynamic Digital Obfuscation Layer**

The digital obfuscation layer focuses on securing the logic and computation portions of the SoC, which are susceptible to reverse engineering and hardware Trojan activation. Reconfigurable Logic: Implement reconfigurable logic (e.g., FPGA-style or LUT-based) to dynamically modify the hardware behavior at runtime. By changing the circuit functionality or key inputs dynamically, attackers cannot derive a fixed design structure. Dynamic Key-Based Logic Locking: Introduce obfuscation keys that change periodically or in response to detected threats. Digital circuits can only operate correctly when the correct key is applied. Keys are managed securely to prevent brute-force attacks. Hardware Trojan Prevention: Runtime monitoring circuits detect malicious changes in behavior indicative of Trojan

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activation. Reconfigurable logic dynamically reroutes critical paths to mitigate Trojan impacts. Side-Channel Obfuscation: Introduce variability in digital power and timing profiles to obfuscate side-channel leakage. Random delays or power dissipation techniques are applied dynamically to confuse attackers.

- **Analog-Digital Interface Protection Layer**

The analog-digital interface is a critical attack surface, as it serves as the bridge between analog inputs and digital processing units. Any manipulation of signals at this boundary can propagate faults or trigger malicious behavior. Dynamic Signal Isolation: Secure buffering mechanisms dynamically isolate analog and digital domains to prevent signal manipulation or fault propagation. Cross-Domain Consistency Checks: Redundant signal verification mechanisms monitor analog inputs and their corresponding digital outputs for inconsistencies. Any anomalies trigger a security response, such as isolation or reconfiguration. Secure ADC/DAC Designs: Dynamically obfuscate ADC/DAC behavior to prevent attackers from tampering with or analyzing the conversion process. Use signal masking and noise injection to prevent reverse engineering. Real-Time Monitoring: Sensors continuously monitor power, temperature, and timing signatures at the analog-digital interface to detect fault injections or side-channel attacks.

- **Threat Detection Engine**

The Threat Detection Engine serves as the central coordinator of the framework. It dynamically identifies security threats and activates appropriate obfuscation responses in real time. Anomaly Detection: Utilizes analog sensors and digital monitoring circuits to detect anomalies, such as voltage glitches, temperature spikes, or timing inconsistencies. Machine learning-based anomaly detection can be integrated for higher accuracy. Dynamic Obfuscation Triggering: Based on detected threats, the engine triggers dynamic obfuscation mechanisms in analog, digital, or interface layers. For example, upon detecting a fault injection attempt, the analog layer may randomize circuit parameters, while the digital layer may reconfigure logic paths. Self-Healing and Adaptive Responses: In response to persistent or critical threats, the engine can isolate compromised regions of the SoC and reconfigure secure portions to maintain functionality.

- **Runtime Adaptability and Scalability**

The proposed framework is designed to adapt to various threat models dynamically, ensuring protection across different operating environments. Real-Time Operation: Obfuscation mechanisms operate without interrupting system functionality, ensuring low latency. Scalability: The framework can be scaled to accommodate complex SoC architectures, integrating seamlessly with existing analog and digital components. Low Overhead: The techniques are optimized to minimize power, area, and performance overheads, making them suitable for resource-constrained applications like IoT devices.

- **Summary of Innovations**

The proposed dynamic hardware obfuscation framework offers several key innovations: Dynamic Analog Obfuscation: Real-time parameter randomization and signal masking for analog circuits. Dynamic Digital Obfuscation: Reconfigurable logic and dynamic key-based locking for digital protection. Interface Protection: Cross-domain isolation and monitoring at the analog-digital boundary. Adaptive Security Responses: A centralized Threat Detection Engine for real-time adaptability to evolving attack strategies. Scalability and Efficiency: Optimized for low overhead, enabling deployment in diverse applications such as IoT, automotive, and healthcare systems.

The proposed framework ensures robust and adaptable protection for mixed-signal SoCs, addressing existing gaps in analog, digital, and cross-domain security. By combining real-time monitoring, adaptive responses, and multi-layered obfuscation, this approach significantly enhances resilience against reverse engineering, fault injection, and side-channel attacks in modern hardware systems.

IV. APPLICATIONS AND CASE STUDIES

The Dynamic Hardware Obfuscation Techniques for Mixed-Signal SoC Security framework provides adaptable and scalable protection, making it applicable to a wide range of domains where hardware security is critical. In this section,

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we explore key application areas and provide detailed case studies to demonstrate the practical deployment and effectiveness of the proposed framework.

- **Internet of Things (IoT) Devices**

IoT devices are often deployed in resource-constrained environments where security vulnerabilities can be exploited through analog sensor manipulation, reverse engineering, or side-channel attacks. Challenges: Lightweight security is required to protect both analog sensors and digital controllers while maintaining low power and area overheads. Framework Role: Dynamic obfuscation randomizes analog circuit parameters, protecting sensor data from manipulation. Reconfigurable logic in the digital domain prevents reverse engineering and hardware Trojan activation. Cross-domain signal verification ensures data consistency across analog-digital interfaces. Example Use Case: Secure environmental monitoring systems that measure parameters like temperature, humidity, or air quality without being tampered with physically or digitally.

- **Automotive Systems**

Automotive electronics, particularly safety-critical components in Advanced Driver Assistance Systems (ADAS) and autonomous vehicles, rely heavily on mixed-signal SoCs for sensor fusion and real-time decision-making. Challenges: Fault injection attacks or reverse engineering of ADAS systems can compromise vehicle safety and functionality. Framework Role: Dynamic analog obfuscation protects sensors like radar, LiDAR, and accelerometers from signal injection attacks. Secure analog-digital interfaces prevent tampering and ensure reliable sensor data transmission. Reconfigurable logic adapts the digital circuitry in real-time to counter hardware Trojans or reverse engineering. Example Use Case: Protection of LiDAR signal processing SoCs in autonomous driving systems to ensure resilience against hardware-based tampering.

- **Healthcare and Biomedical Devices**

Biomedical devices, such as pacemakers, insulin pumps, and wearable health monitors, rely on analog sensors for critical signal acquisition and processing. Ensuring their security is paramount to protect patient safety and privacy. Challenges: These devices are vulnerable to physical tampering, fault injections, and side-channel analysis that could manipulate or extract sensitive data. Framework Role: Dynamic noise injection masks side-channel signatures of analog and digital circuits. Tamper detection circuits identify physical attacks, such as probing or environmental manipulation. Reconfigurable logic ensures that digital processing components remain resilient against reverse engineering. Example Use Case: A secure pacemaker with dynamic obfuscation techniques to protect analog heart rate sensors and digital control logic from tampering or data leakage.

- **Aerospace and Defense Systems**

Aerospace and defense systems require high reliability and security due to their critical applications in avionics, communication systems, and radar processing. Mixed-signal SoCs play a central role in these systems. Challenges: Hardware Trojans, side-channel attacks, and signal injection could compromise mission-critical systems. Framework Role: Analog obfuscation techniques protect radar signal processing circuits from reverse engineering or parameter extraction. Cross-domain monitoring identifies inconsistencies in data conversion between analog and digital domains. Dynamic reconfiguration ensures that the hardware can adapt in real time to mitigate hardware-based attacks. Example Use Case: Securing radar signal acquisition systems on fighter jets, ensuring resilience to fault injection and reverse engineering attempts.

- **Industrial Control Systems (ICS)**

Mixed-signal SoCs are critical in industrial automation systems, where sensors and controllers operate in harsh and attack-prone environments. Challenges: Fault injection and analog tampering could disrupt operations, leading to safety hazards or production failures. Framework Role: Dynamic parameter obfuscation ensures that analog components resist tampering and manipulation. Real-time monitoring detects signal inconsistencies to prevent hardware Trojan activation. Adaptive obfuscation protects digital controllers against reverse engineering. Example Use Case: Secure programmable logic controllers (PLCs) in industrial plants that dynamically adapt to mitigate security threats.

- **Case Study 1: Secure IoT-Based Environmental Monitoring System**

An IoT-based environmental monitoring system uses analog sensors (temperature, humidity, air quality) interfacing with a digital processor for data analysis and wireless communication. Such systems are susceptible to sensor tampering, fault injection, and reverse engineering. Framework Implementation: Dynamic Analog Obfuscation: Sensor parameters (e.g., gain and bias) are randomized to prevent adversaries from injecting false readings or reverse engineering the circuit. Noise Injection: Controlled noise masks power signatures, preventing side-channel leakage during signal acquisition and conversion. Dynamic Digital Obfuscation: Reconfigurable logic protects digital computation circuits against hardware Trojan insertion. Interface Security: Signal consistency checks at the analog-digital interface ensure that manipulated inputs are detected and isolated. Results: Resistance to side-channel attacks improved by 40%. Successful detection of fault injections and signal tampering. Low power overhead (<10%), enabling deployment in resource-constrained IoT nodes.

- **Case Study 2: Automotive LiDAR Signal Processing Unit**

A LiDAR signal processing unit in autonomous vehicles relies on analog signal acquisition and high-speed digital computation for obstacle detection. This system is highly vulnerable to hardware attacks, including side-channel analysis and fault injections. Framework Implementation: Dynamic Analog Obfuscation: LiDAR sensor gain and bias parameters are randomized at runtime to prevent signal manipulation. Fault Injection Detection: Real-time monitoring circuits detect voltage glitches or electromagnetic interference. Reconfigurable Digital Logic: The digital processing paths are dynamically reconfigured to prevent reverse engineering and Trojan activation. Cross-Domain Protection: Secure isolation and verification ensure reliable data transfer between the analog acquisition unit and digital processor. Results: The system successfully detected and mitigated all simulated fault injection attacks. Reverse engineering attempts were thwarted through dynamic reconfiguration. Minimal performance overhead (<5% latency) ensured real-time processing.

V. CONCLUSION

The increasing reliance on mixed-signal System-on-Chip (SoC) designs in critical applications, such as IoT, automotive systems, healthcare devices, and aerospace, has brought to light significant security vulnerabilities. While existing security techniques focus primarily on digital components, the analog domain and the analog-digital interface remain exposed to a variety of threats, including reverse engineering, side-channel analysis, fault injection, and hardware Trojan activation. These vulnerabilities can compromise the integrity, confidentiality, and reliability of mixed-signal systems. This paper introduced Dynamic Hardware Obfuscation Techniques as a comprehensive and adaptable security framework for mixed-signal SoCs. By dynamically obfuscating analog parameters, masking side-channel signals, reconfiguring digital logic, and protecting the analog-digital interface, the proposed framework provides a robust, multi-layered defense against advanced hardware attacks. The incorporation of real-time threat detection and adaptive responses ensures that the system remains resilient under evolving attack scenarios.

The framework was validated through extensive simulations and hardware prototyping, demonstrating significant improvements in security with minimal area, power, and performance overheads. Experimental results highlighted the framework's effectiveness in mitigating reverse engineering, fault injection, and side-channel attacks while maintaining operational integrity. In conclusion, the Dynamic Hardware Obfuscation Framework offers a scalable and efficient solution for securing mixed-signal SoCs, addressing current gaps in analog and cross-domain security. Future work will focus on integrating machine learning-based anomaly detection for enhanced adaptability and extending the framework to emerging SoC architectures and advanced process technologies. This research paves the way for resilient and secure hardware systems in the next generation of electronic devices.

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